

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2822

Examiner: Soward, Ida M.

Serial No.: 10/629,337

Filed: July 28, 2003

In re Application of: John McCollum

For: TRANSISTOR HAVING FULLY-DEPLETED JUNCTIONS TO REDUCE
CAPACITANCE AND INCREASE RADIATION IMMUNITY IN AN
INTEGRATED CIRCUIT

APPLICANT'S COMMENTS ON REASONS FOR ALLOWANCE

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450

Dear Sir:

These comments are responsive to the Office Action mailed on June 6, 2005, which sets forth the Examiner's statement of reasons for allowance.

The Examiner is thanked for the kind allowance of all pending claims (i.e. claims 8, 9, 17, 18, 26, 27, 35 and 36). Applicants acknowledge the Examiner's statement of reasons for allowance as set forth in the Notice of Allowance. However, Applicants point out that the reasons for allowablity of the above referenced claims are not limited to the reasons for allowance as set forth in the Notice of Allowance, and that additional reasons for allowablity may exist, each of which may be independently sufficient to establish the patentability of one or more pending claims.

Applicants respectfully reserve the right to introduce, articulate, or otherwise comment on any such additional reasons for allowance as may be appropriate in any future proceedings concerning the claimed invention.

Please charge any additional required fee or credit any overpayment to our deposit account number 50-0612.

Respectfully submitted,
SIERRA PATENT GROUP, LTD.

Dated: August 30, 2006

/jonathan d. hanish/

Jonathan D. Hanish
Reg. No. 57,821

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TRANSMITTAL OF FORMAL DRAWINGS

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

With respect to the above-identified patent application, enclosed herewith for filing **are four** sheets of replacement, Formal Drawings.

Respectfully submitted,
Sierra Patent Group, Ltd.

Dated: August 30, 2006

/jonathan d. hanish/

Jonathan D. Hanish
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